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9/15/03
Mullish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/987,978

Filed: 16 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: Jasmine Jhihan B. CLARK

Conf. No.: 7905

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AMENDMENT

MS - Reissue
Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

16 November 2001

Sir:

In response to the Office Action having a USPTO mailing date of
28 March 2003, the following is respectfully submitted in connection with the above-
identified application.

The following amendments are made in accordance with the rules with
Reissue applications.

09/15/2002 CMELLISH 06000001 012135 09987978
01 FC:1252 410.00 DA